

D15	20	19	D14
D13	18	17	D12
D11	16	15	D10
D9	14	13	D8
D7	12	11	D6
D5	10	9	D4
D3	8	7	D2
D1	6	5	D0
GND	4	3	GND
GND	2	1	GND

Jpdata

A23	26	25	A22
A21	24	23	A20
A19	22	21	A18
A17	20	19	A16
A15	18	17	A14
A13	16	15	A12
A11	14	13	A10
A9	12	11	A8
A7	10	9	A6
A5	8	7	A4
A3	6	5	A2
A1	4	3	A0/NL B
GND	2	1	GND

Jpaddress

/CS4	20	19	/CS5
/CS6	18	17	/CS2
NRS1	16	15	A0/NL B
/CS3	14	13	/CS2
NWAIT	12	11	MCK0
NRP1/NUB	10	9	NRD/NOE
GND	8	7	NLR0/NLE
GND	6	5	GND
GND	4	3	GND
GND	2	1	GND

Jpcontrol

NSPICS6	20	19	NSPICS7
NSPICS5	18	17	NSPICS8
PA25/MOSI	16	15	PA23/SPCK
PA22/RXD2	14	13	PA24/MISO
PA20/SCK2	12	11	PA21/TXD2
PA18/TXD1/NTR1	10	9	PA19/RXD1
PA16/RXD0	8	7	PA12/SCK1
PA14/SCK0	6	5	PA15/TXD0
GND	4	3	GND
GND	2	1	GND

Jpcomm

PB7/AD1IRIG	26	25	GND
AD7	24	23	GND
AD6	22	21	PB18/BMS
AD5	20	19	PB2
AD4	18	17	PB1
AD3	16	15	PB0
AD2	14	13	PB5
AD1	12	11	PB4/IRQ5
AD0	10	9	PB3/IRQ4
PB6/AD0TRIG	8	7	PA13/FIQ
DA1	6	5	PA12/IRQ3
DA0	4	3	PA11/IRQ2
PB16	2	1	PA10/IRQ1

Jpad/da

PA8/TIOB5	20	19	PA2/TIOA5
PA6/ICLK5	18	17	PA5/TIOB4
PA4/TIOA4	16	15	PA3/ICLK4
PA2/TIOB3	14	13	PA1/TIOA3
PA0/ICLK3	12	11	PB22/TIOB2
PB26/TIOA2	10	9	PB25/ICLK2
PB24/TIOB1	8	7	PB23/TIOA1
PB22/ICLK1	6	5	PB21/TIOB0
GND	4	3	GND
GND	2	1	GND

Jptimer

Notes:
If a cable is connected to JpControl NWAIT must be pulled high through a 100K resistor
Jpdata, Jpaddress, and JpControl are buffered
JpComm, JPAD/DA, and JPTimer are unbuffered.

TITLE: BufferBoardPins

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REV:

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